**Practical 4: RPI-3B SPI and Interrupt**

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**Question 1:**

1. SPI communication protocol works in a synchronous system where both devices (slave and master) share a common clock signal. The clock signal synchronizes the master output data bits to the sampling bits of the slave. One bit of data is transferred per clock cycle therefore the speed of data transfer is determined by the frequency of the clock.
2. This is an interrupt